# Exhibit 20

T-Mobile Smartphones (see product list at end for relevant models) Infringement of the '242 patent		
Claim 1	Evidence	
1. A method of processing imaging signals, the method comprising:	The T-Mobile smartphone performs a method of processing imaging signals.	
	For example, the T-Mobile smartphone includes an image capturing subsystem, an image processing subsystem, and an interface subsystem connecting them. The image processing subsystem processes imaging signals that are received from the image capturing subsystem via the interface subsystem.	
receiving image data from an imaging array;	The T-Mobile smartphone receives image data from an imaging array.	
	For example, the image capturing subsystem includes a CMOS image sensor that includes an imaging array. The imaging array produces image data when exposed to an image. The interface subsystem of the T-Mobile smartphone receives the image data from the imaging array.	
storing the image data in a FIFO memory;	The T-Mobile smartphone stores the image data in a FIFO memory.	
	For example, the interface subsystem includes a FIFO memory for storing image data. The image data received from the imaging array is stored in the FIFO memory by the interface subsystem.	
updating a FIFO counter to maintain a count of the image data in the FIFO memory in response to	The T-Mobile smartphone updates a FIFO counter to maintain a count of the image data in the FIFO memory in response to memory reads and writes;	
memory reads and writes;	For example, the interface subsystem includes a FIFO counter to maintain a count of the image data, or "fill level", that is stored in the FIFO memory. When a unit of image data is written to the FIFO memory, the count of the FIFO counter is incremented. When a unit of image data is read from the FIFO memory, the count of the FIFO counter is decremented.	
comparing the count of the FIFO counter with a FIFO limit;	The T-Mobile smartphone compares the count of the FIFO counter with a FIFO limit.	

For example, the interface subsystem includes a FIFO limit which it compares to the FIFO count to determine if the amount of image data in the FIFO memory is at a "fill level" that will require the interface subsystem to take an action.  generating an interrupt signal to request a processor to transfer image data from the FIFO memory in response to an interrupt enable signal being valid and the count of the FIFO counter having a predetermined relationship to the FIFO limit, and  For example, the interface subsystem includes a processor for performing operations to transmit image data to the image processing subsystem. The servicing of interrupts by the processor can be enabled or disabled. When the servicing of interrupts from the FIFO memory is enabled and the count of the FIFO limit, the interface subsystem generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory.  The T-Mobile smartphone transfers image data from the FIFO memory to the processor in response to the interrupt signal.  For example, when the processor receives the interrupt signal, the processor transfers the image data from the FIFO memory to the processor, which transmits the image data to the image processing subsystem for processing.		
request a processor to transfer image data from the FIFO memory in response to an interrupt enable signal being valid and the count of the FIFO counter having a predetermined relationship to the FIFO limit; and  for performing operations to transmit image data to the image processor can be enabled or disabled. When the servicing of interrupts from the FIFO memory is enabled and the count of the FIFO counter having a predetermined relationship to the FIFO limit; and  for performing operations to transmit image data to the image processing subsystem. The servicing of interrupts by the processor can be enabled or disabled. When the servicing of interrupts from the FIFO memory is enabled and the count of the FIFO counter has a predetermined relationship to the FIFO limit, the interface subsystem generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory.  The T-Mobile smartphone transfers image data from the FIFO memory to the processor in response to the interrupt signal.  For example, when the processor receives the interrupt signal, the processor transfers the image data from the FIFO memory to the processor, which transmits the image		which it compares to the FIFO count to determine if the amount of image data in the FIFO memory is at a "fill level"
memory in response to an interrupt enable signal being valid and the count of the FIFO counter having a predetermined relationship to the FIFO limit.  For example, the interface subsystem includes a processor for performing operations to transmit image data to the image processing subsystem. The servicing of interrupts by the processor can be enabled or disabled. When the servicing of interrupts from the FIFO memory is enabled and the count of the FIFO limit, the interface subsystem generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory to the processor in response to the interrupt signal, the processor transfers the image data from the FIFO memory to the processor receives the interrupt signal, the processor, which transmits the image	generating an interrupt	The T-Mobile smartphone generates an interrupt signal to
image data from the FIFO memory in response to an interrupt enable signal being valid and the count of the FIFO counter having a predetermined relationship to the FIFO counter having a predetermined relationship to the FIFO limit; and  For example, the interface subsystem includes a processor for performing operations to transmit image data to the image processing subsystem. The servicing of interrupts by the processor can be enabled or disabled. When the servicing of interrupts from the FIFO memory is enabled and the count of the FIFO counter has a predetermined relationship to the FIFO limit, the interface subsystem generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory.  The T-Mobile smartphone transfers image data from the FIFO memory to the processor in response to the interrupt signal.  For example, when the processor receives the interrupt signal, the processor transfers the image data from the FIFO memory to the processor, which transmits the image	signal to request a	request a processor to transfer image data from the FIFO
memory in response to an interrupt enable signal being valid and the count of the FIFO counter having a predetermined relationship to the FIFO limit; and  For example, the interface subsystem includes a processor for performing operations to transmit image data to the image processing subsystem. The servicing of interrupts by the processor can be enabled or disabled. When the servicing of interrupts from the FIFO memory is enabled and the count of the FIFO counter has a predetermined relationship to the FIFO limit, the interface subsystem generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory.  The T-Mobile smartphone transfers image data from the FIFO memory to the processor in response to the interrupt signal.  For example, when the processor receives the interrupt signal, the processor transfers the image data from the FIFO memory to the processor, which transmits the image	processor to transfer	memory in response to an interrupt enable signal being
interrupt enable signal being valid and the count of the FIFO counter having a predetermined relationship to the FIFO limit; and limit; and relationship to the FIFO counter has a predetermined relationship to the FIFO limit, the interface subsystem generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory.  The T-Mobile smartphone transfers image data from the FIFO memory to the processor in response to the interrupt signal.  For example, when the processor receives the interrupt signal, the processor, which transmits the image	image data from the FIFO	valid and the count of the FIFO counter having a
being valid and the count of the FIFO counter having a predetermined relationship to the FIFO limit; and servicing of interrupts from the FIFO memory is enabled and the count of the FIFO limit, the interface subsystem generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory.  The T-Mobile smartphone transfers image data from the FIFO memory to the processor in response to the interrupt signal.  For example, the interface subsystem includes a processor for performing operations to transmit image data to the image processing subsystem. The servicing of interrupts by the processor can be enabled or disabled. When the servicing of interrupts interrupts from the FIFO memory is enabled and the count of the FIFO counter has a predetermined relationship to the FIFO limit, the interface subsystem generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory to the processor in response to the interrupt signal.  For example, when the processor receives the interrupt signal, the processor transfers the image data from the FIFO memory to the processor, which transmits the image	memory in response to an	predetermined relationship to the FIFO limit.
for performing operations to transmit image data to the image processing subsystem. The servicing of interrupts by the processor can be enabled or disabled. When the servicing of interrupts from the FIFO memory is enabled and the count of the FIFO counter has a predetermined relationship to the FIFO limit, the interface subsystem generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory.  Transferring image data from the FIFO memory to the processor in response to the interrupt signal.  The T-Mobile smartphone transfers image data from the FIFO memory to the processor in response to the interrupt signal.  For example, when the processor receives the interrupt signal, the processor transfers the image data from the FIFO memory to the processor, which transmits the image	interrupt enable signal	
having a predetermined relationship to the FIFO limit; and image processing subsystem. The servicing of interrupts by the processor can be enabled or disabled. When the servicing of interrupts from the FIFO memory is enabled and the count of the FIFO counter has a predetermined relationship to the FIFO limit, the interface subsystem generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory.  The T-Mobile smartphone transfers image data from the FIFO memory to the processor in response to the interrupt signal.  For example, when the processor receives the interrupt signal, the processor transfers the image data from the FIFO memory to the processor, which transmits the image	being valid and the count	For example, the interface subsystem includes a processor
the processor can be enabled or disabled. When the servicing of interrupts from the FIFO memory is enabled and the count of the FIFO counter has a predetermined relationship to the FIFO limit, the interface subsystem generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory.  The T-Mobile smartphone transfers image data from the FIFO memory to the processor in response to the interrupt signal.  For example, when the processor receives the interrupt signal, the processor transfers the image data from the FIFO memory to the processor, which transmits the image	of the FIFO counter	for performing operations to transmit image data to the
servicing of interrupts from the FIFO memory is enabled and the count of the FIFO counter has a predetermined relationship to the FIFO limit, the interface subsystem generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory.  The T-Mobile smartphone transfers image data from the FIFO memory to the processor in response to the interrupt signal.  For example, when the processor receives the interrupt signal, the processor transfers the image data from the FIFO memory to the processor, which transmits the image	having a predetermined	image processing subsystem. The servicing of interrupts by
and the count of the FIFO counter has a predetermined relationship to the FIFO limit, the interface subsystem generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory.  The T-Mobile smartphone transfers image data from the FIFO memory to the processor in response to the interrupt signal.  For example, when the processor receives the interrupt signal, the processor transfers the image data from the FIFO memory to the processor, which transmits the image	relationship to the FIFO	the processor can be enabled or disabled. When the
relationship to the FIFO limit, the interface subsystem generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory.  The T-Mobile smartphone transfers image data from the FIFO memory to the processor in response to the interrupt signal.  For example, when the processor receives the interrupt signal, the processor transfers the image data from the FIFO memory to the processor, which transmits the image	limit; and	
generates an interrupt signal. The interrupt signal represents a request for the processor to transfer image data from the FIFO memory.  The T-Mobile smartphone transfers image data from the FIFO memory to the processor in response to the interrupt signal.  For example, when the processor receives the interrupt signal, the processor transfers the image data from the FIFO memory to the processor, which transmits the image		·
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For example, when the processor receives the interrupt signal, the processor transfers the image data from the FIFO memory to the processor, which transmits the image		signal.
signal, the processor transfers the image data from the FIFO memory to the processor, which transmits the image		For example, when the processor receives the interrupt
FIFO memory to the processor, which transmits the image		
, , ,		
		data to the image processing subsystem for processing.

T-Mobile smartphone Infringement of the '242 patent		
Claim 8	Evidence	
8. A method of processing imaging signals, the method comprising:	The T-Mobile smartphone performs a method of processing imaging signals.	
•	For example, the T-Mobile smartphone includes an image capturing subsystem, an image processing subsystem and an interface subsystem connecting them. The image processing subsystem processes imaging signals that are received from the image capturing subsystem via the interface subsystem.	
receiving image data from an imaging array;	The T-Mobile smartphone receives image data from an imaging array.	
	For example, the image capturing subsystem includes a CMOS image sensor that includes an imaging array. The imaging array produces image data when exposed to an image. The interface subsystem of the T-Mobile smartphone receives the image data from the imaging array.	
storing the image data in a FIFO memory;	The T-Mobile smartphone stores the image data in a FIFO memory.	
	For example, the interface subsystem includes a FIFO memory for storing image data. The image data received from the imaging array is stored in the FIFO memory by the interface subsystem.	
updating a FIFO counter to maintain a count of the image data in the FIFO memory in response to	The T-Mobile smartphone updates a FIFO counter to maintain a count of the image data in the FIFO memory in response to memory reads and writes;	
memory reads and writes;	For example, the interface subsystem includes a FIFO counter to maintain a count of the image data, or "fill level", that is stored in the FIFO memory. When a unit of image data is written to the FIFO memory, the count of the FIFO counter is incremented. When a unit of image data is read from the FIFO memory, the count of the FIFO counter is decremented.	
comparing the count of the FIFO counter with a FIFO limit;	The T-Mobile smartphone compares the count of the FIFO counter with a FIFO limit.	
	For example, the interface subsystem includes a FIFO limit which it compares to the FIFO count to determine if the	

	amount of image data in the FIFO memory is at a "fill level" that will require the interface subsystem to take an action.
generating, in response to the count of the FIFO counter having a predetermined relationship to the FIFO limit, a bus request signal to request a	The T-Mobile smartphone generates, in response to the count of the FIFO counter having a predetermined relationship to the FIFO limit, a bus request signal to request a bus arbitration unit to grant access to an output bus.
bus arbitration unit to grant access to an output bus; and	For example, the interface subsystem includes a bus arbitration unit and an output bus to which the image processing subsystem is connected. When the count of the FIFO counter has a predetermined relationship to the FIFO limit, the interface subsystem generates a bus request signal. The bus request signal represents a request for the bus arbitration unit to grant the interface subsystem access to the output bus.
transferring image data from the FIFO memory to the output bus in response to receiving a grant signal	The T-Mobile smartphone transfers image data from the FIFO memory to the output bus in response to receiving a grant signal from the bus arbitration unit.
from the bus arbitration unit.	For example, after the bus arbitration unit receives the bus request signal it generates a grant signal that gives the interface subsystem access to the output bus. Upon receiving the grant signal, the image data is transferred from the FIFO memory to the output bus for processing by the image processing subsystem.

## **Product List:**

REVVL 5G TD-LTE US 128GB T790W / T790Z
REVVL 4+ TD-LTE US 5062W / 5062Z
REVVL 4 LTE US 5007W / 5007Z
REVVLRY TD-LTE US 32GB XT1952-T
REVVLRY+ TD-LTE US XT1965-T
Revvl 2 Plus LTE US 6062Z
Revvl 2 LTE US 5052W
REVVL Plus LTE US
REVVL LTE US

## **References:**

#### [1] REVVL 5G TD-LTE US 128GB T790W / T790Z

http://phonedb.net/index.php?m=device&id=17410&c=t-mobile revvl 5g td-lte us 128gb t790w t790z tcl t1b 5g&d=detailed specs#section14

# [2] REVVL 4+ TD-LTE US 5062W / 5062Z

http://phonedb.net/index.php?m=device&id=17409&c=t-mobile revvl 4plus td-lte us 5062w 5062z tcl 5062

# [3] REVVL 4 LTE US 5007W / 5007Z

http://phonedb.net/index.php?m=device&id=17408&c=tmobile revvl 4 lte us 5007w 5007z tcl 5007b&d=detailed specs#section14

# [4] REVVLRY TD-LTE US 32GB XT1952-T

http://phonedb.net/index.php?m=device&id=15348&c=t-mobile revvlry tdlte us 32gb xt1952-t motorola channel

# [5] REVVLRY+ TD-LTE US XT1965-T

http://phonedb.net/index.php?m=device&id=15345&c=t-mobile\_revvlryplus\_td-lte\_us\_xt1965-t\_motorola\_lake

### [6] Revvl 2 Plus LTE US 6062Z

http://phonedb.net/index.php?m=device&id=14402&c=t-mobile revvl 2 plus Ite us 6062z tcl 6062

#### [7] Revvl 2 LTE US 5052W

http://phonedb.net/index.php?m=device&id=14401&c=t-mobile revvl 2 lte us 5052w tcl 5052

## [8] REVVL Plus LTE US

http://phonedb.net/index.php?m=device&id=12805&c=t-mobile revvl plus lte us&d=detailed specs

#### [9] REVVL LTE US

http://phonedb.net/index.php?m=device&id=11937&c=t-mobile revvl lte us&d=detailed specs